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EP138975 American european software patent - Simultaneous ...

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EP1030468 Ico european software patent - Communication apparatus ...

... for use with the GSM system, comprising a digital low rate coder/**decoder** 30, ... in the same manner as the above-described **demodulator** and **remodulator**, ...
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... audio signal from the **demodulator** 36 is applied to an audio **decoder** 42. ...
remodulator 44 and may also be provided at output terminals 46 and 48. ...
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... In order to form these quantities, **remodulator** 67, like **demodulator** 57, ... for example) is accomplished by **subtracting** (2i+1) from the pointer to ...
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... The **demodulator** 28 converts the received RF signal to ... processed further in an error correction **decoder** 44 which ... is also applied to a recoder/**remodulator** 46 to ...
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... m). In order to form these quantities, **remodulator** 67, like ... the count theretofore loaded into it from **decoder** 251 ... be formed at the output of **demodulator** 57, 2M ...

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2. [Patent 4245345: Timing acquisition in voiceband data sets](#)
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CHEN, Ernest, C. / WANG, Weizheng / ZHOU, Guangcai / LIN, Tung-Sheng / SANTORU, Joseph / THE DIRECTV GROUP, INC., PATENT COOPERATION TREATY APPLICATION, May 2004
...for multiple encoders/**decoders**. The present invention...integrated receiver/**decoder** (IRD) 500 (also hereinafter...500 comprises a tuner/**demodulator** 504 communicatively...distribution system. The tuner/**demodulator** 504 isolates a single...error correction (FEC) **decoder** 506. This allows the...
Full text available at patent office. For more in-depth searching go to LexisNexis® similar results
- 2. [IMPROVING HIERARCHICAL 8PSK PERFORMANCE](#)
CHEN, Ernest, C. / SANTORU, Joseph / HUGHES ELECTRONICS CORPORATION, PATENT COOPERATION TREATY APPLICATION, Jan 2004
...coupled to the **remodulator** and the first **demodulator**, for **subtracting** the remodulated...second symbol **decoder**, communicatively...and second) **demodulators**, symbol **decoders**, and error...the second **demodulator** 216 and second symbol **decoder** 220 are shown...
Full text available at patent office. For more in-depth searching go to LexisNexis® similar results
- 3. [ESTIMATING THE OPERATING POINT ON A NONLINEAR TRAVELING WAVE TUBE AMPLIFIER](#)
CHEN, Ernest, C. / MAITRA, Shamik / THE DIRECTV GROUP, INC., PATENT COOPERATION TREATY APPLICATION, May 2004
...Such a scaling may be conducted 25 by **subtracting** a measured input operating point value...the scaling may also be conducted by **subtracting** a measured output operating point value...The scaling may also be conducted by **subtracting** a measured phase value at the output...
Full text available at patent office. For more in-depth searching go to LexisNexis® similar results
- 4. [MAXIMIZING POWER AND SPECTRAL EFFICIENCIES FOR LAYERED AND CONVENTIONAL MODULATIONS](#)
CHEN, Ernest, C. / THE DIRECTV GROUP, INC., PATENT COOPERATION TREATY APPLICATION, May 2004
...layer signal can be demodulated after **subtracting** the upper layer signal from the layered...legacy systems, while maintaining receiver/**decoder** backward compatibility.

The required...block diagram of an integrated receiver/**decoder** (IRD) [0026]
FIGS. 6A - 6C...

Full text available at patent office. For more in-depth searching go to LexisNexis-similar results

5. FEEDER LINK CONFIGURATIONS TO SUPPORT LAYERED MODULATION

ANDERSON, Paul, R. / SANTORU, Joseph / CHEN, Ernest, C. / THE DIRECTV GROUP, INC., PATENT COOPERATION TREATY APPLICATION, May 2004

...one 15 integrated receiver/**decoder** (IRD). The system includes...least one integrated receiver/**decoder** (IRD). The system further includes...one integrated 10 receiver/**decoder** (IRD). A second modulator modulates...higher-order modulation receiver/**demodulator** for receiving and demodulating...

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6. METHOD AND APPARATUS FOR TAILORING CARRIER POWER REQUIREMENTS ACCORDING TO AVAILABILITY IN LAYERED MODULATION SYSTEMS

CHEN, Ernest, C. / ANDERSON, Paul, R. / SANTORU, Joseph / HUGHES ELECTRONICS CORPORATION, PATENT COOPERATION TREATY APPLICATION, May 2004

...integrated receiver/**decoder** FIGs. 6A - 6C are diagrams...integrated receiver/**decoder** (IRD) 500 (also hereinafter...comprises a I tuner/**demodulator** 504 communicatively...distribution system. The tuner/**demodulator** 504 isolates a single...error correction (FEC) **decoder** 506. This allows the...

Full text available at patent office. For more in-depth searching go to LexisNexis-similar results

7. AMPLITUDE AND PHASE MATCHING FOR LAYERED MODULATION RECEPTION

CHEN, Ernest, C. / CHEN, Jeng-Hong / SHUM, Kenneth / OH, Joungheon / HUGHES ELECTRONICS CORPORATION, PATENT COOPERATION TREATY APPLICATION, May 2004

...**demodulator** 504 and **decoder** 508 can be...synthesizer or **demodulator** 514. The **remodulator** 514 receives...upper I layer **demodulator** 504 and the...from the **decoder** 508. 5 To...signal, the **remodulator** 514 can also...504 and the **remodulator** 514. The...lower layer **demodulator** 528 and **decoder** 530 to I...

Full text available at patent office. For more in-depth searching go to LexisNexis-similar results

8. POWER DIVISION MULTIPLEXING WITH INCOHERENT SIGNALS AND FIXED POWDER HIERARCHY

CHEN, Ernest, C. / HUGHES ELECTRONICS CORPORATION, PATENT COOPERATION TREATY APPLICATION, Nov 2002

...frequency. **DEMODULATOR AND DECODER** FIGs. 4A...processed by a **demodulator** 404 to produce...fed to a **decoder** 402 which...fed to a **remodulator** 406 which...output of the **demodulator** 404 to the FEC **decoder** 402 after...t). After **subtracting** the upper...

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9. SELECTIVE JOINT DEMODULATION SYSTEMS AND METHODS FOR RECEIVING A SIGNAL IN THE PRESENCE OF NOISE AND INTERFERENCE

MOLNAR, Karl, J. / Ericsson Inc., EUROPEAN PATENT, Sep 2002

...diagram of **demodulators** and **decoders** of Figure...includes a **demodulator** and **decoder** 310 that is...embodiment of the **demodulator** and **decoder** 310 of Figure...remodulated at **remodulator** 418 and the...adder 422 by' **subtracting** the desired...

Full text available at patent office. For more in-depth searching go to LexisNexis-similar results

10. TRAFFIC VERIFICATION SYSTEM

PAGES, Jeffrey, L. / ENGLISH, Glen, F. / INNES CORPORATION PTY. LTD., PATENT COOPERATION TREATY APPLICATION, Dec 1999

...filter used in the **decoder** of the present...traditional MSK **demodulator**. Figure 7b

shows...delay line 110 and **subtracting** element 115) has...which spans the **decoder** filter's guardband...reaching the data **demodulator**, the bandwidth...stopband in the **decoder** as shown in Figure...

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1. **On $\Sigma\Delta$ signal processing remodulator complexity**
Kershaw, S.M.; Summerfield, S.; Sandler, M.B.;
Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium on
Volume 2, 28 April-3 May 1995 Page(s):881 - 884 vol.2
[AbstractPlus](#) | Full Text: [PDF\(392 KB\)](#) IEEE CNF
2. **Direct processing of Sigma-Delta signals**
Pneumatakis, A.; Delyannis, T.;
Electronics, Circuits, and Systems, 1996. ICECS '96., Proceedings of the Third IEEE International Conference on
Volume 1, 13-16 Oct. 1996 Page(s):13 - 16 vol.1
[AbstractPlus](#) | Full Text: [PDF\(328 KB\)](#) IEEE CNF
3. **WDM protocol-transparent distance extension using R2 remodulation**
Green, P.E., Jr.; Jannello, F.J.; Ramaswami, R.;
Selected Areas in Communications, IEEE Journal on
Volume 14, Issue 5, June 1996 Page(s):962 - 967
[AbstractPlus](#) | References | Full Text: [PDF\(548 KB\)](#) IEEE JNL
4. **A duplex WDM passive optical network with 1:16 power split using reflective SOA remodulation ONU**
Koponen, J.J.; Soderlund, M.J.;
Optical Fiber Communication Conference, 2004. OFC 2004
Volume 1, 23-27 Feb. 2004
[AbstractPlus](#) | Full Text: [PDF\(299 KB\)](#) IEEE CNF
5. **An exact solution for a notch filter transfer function using an operator formalism**
Piper, J.;
System Theory, 1989. Proceedings., Twenty-First Southeastern Symposium on
26-28 March 1989 Page(s):621 - 623
[AbstractPlus](#) | Full Text: [PDF\(124 KB\)](#) IEEE CNF
6. **VLSI implementation of a sigma-delta bitstream FIR filter**
Summerfield, S.; Anderson, M.; Kershaw, S.; Sandler, M.;
Circuits and Systems, 1996. ISCAS '96., 'Connecting the World', 1996 IEEE International Symposium on
Volume 2, 12-15 May 1996 Page(s):273 - 276 vol.2
[AbstractPlus](#) | Full Text: [PDF\(336 KB\)](#) IEEE CNF
7. **Realisation and implementation of a sigma-delta bitstream FIR filter**

Kershaw, S.M.; Summerfield, S.; Sandler, M.B.; Anderson, M.;
Circuits, Devices and Systems, IEE Proceedings [see also IEE Proceedings G- Circuits, Devices
Systems]
Volume 143, Issue 5, Oct. 1996 Page(s):267 - 273
[AbstractPlus](#) | Full Text: [PDF\(668 KB\)](#) IEE JNL

- 8. **Simultaneous bidirectional signalling using multiquantum well reflective modulator/detector**
Kirkby, C.J.G.; Ash, R.M.; Moseley, A.J.; Carter, A.C.;
Electronics Letters
Volume 27, Issue 25, 5 Dec. 1991 Page(s):2373 - 2374
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